

(Amended) 1. An integrated multiple-substrate-on-chip-module (MSOCM) assembly comprising:

a chip-size package (CSP)-ready MSOCM board having a top surface and a bottom surface, said CSP-ready [MCM] MSOCM board includes a plurality of board bonding-wire windows and said [top surface] CSP-ready MSOCM board further includes a plurality of board-bonding-pads disposed on said top surface of said CSP-ready MSOCM board near said bonding-wire window;

an adhesive layer disposed beneath said CSP-ready [MCM] MSOCM board having also having a plurality of adhesive-layer bonding wire windows corresponding to and aligned with said board bonding wire windows on said [MCM] MSOCM board;

a plurality of integrated circuit (IC) chips mounted onto said adhesive layer under said bottom surface of said CSP-ready [MCM] MSOCM board with each of said IC chips provided with a plurality of chip bonding pads facing an open space defined by said board bonding wire windows; [and]

a plurality of bonding wires disposed in said space defined by said board and adhesive-layer bonding-wire windows and interconnected between each of said chip bonding pads and a corresponding board bonding pad disposed on said top surface of said CSP-ready [MCM] MSOCM board; and

a CSP-ready land-grid array comprising a plurality of land-grid contact pads disposed on said top surface of said CSP-ready MSOCM board wherein said plurality of land-grid contact pads are arranged to have a standard CSP-footprint of said IC chips whereby each of said IC chips mounted onto said CSP-ready MSOCM board is provided to be separated into an individual CSP package.

(Amended) 2. The MSOCM assembly of claim 1 wherein:

said CSP-ready MSOCM board and said adhesive layer further include a plurality CSP-ready separation lines dividing each said IC chips mounted [thereon] onto said bottom surface of said CSP-ready MSOCM board for separating said MSOCM assembly into a plurality of standard individual CSP packages each contain one of said IC chips.

(Amended) 3. The MSOCM assembly of claim 1 wherein:

said CSP-ready MSOCM board further includes a plurality of via connectors penetrating said CSP-ready MSOCM board and in electrical connection with a plurality of said chip bonding pads via metal traces disposed on said bottom surface of said MSOCM board; and

each of said via connectors further being in electric connection with one of said [a] land grid contact pads [array] disposed on said top surface of said MSOCM board.

(Amended) 4. The MSOCM assembly of claim 1 wherein:

said land grid array comprising [a] said plurality of land grid contact pads [CSP-ready connection] constituting CSP-ready solder pads are insulated by a plurality of solder masks disposed between said land grid contact pads.

(Amended) 5. The MSOCM assembly of claim 1 further comprising:

a plurality of solder balls, each mounted on [a plurality] one of said [CSP-ready solder] land grid contact pads on said top surface of said CSP-ready MSOCM board.

(Amended) 6.

The MSOCM assembly of claim 1 further comprising:

a plurality of testing pins including a set of burn-in test pins and a set of board level test pins disposed on an edge of said CSP-ready [MCM] MSOCM board provided for conducting a plurality of burn-in and board level tests.

7. The MSOCM assembly of claim 1 wherein:

each of said board bonding-wire windows further comprising a bigger opening near said top surface of said CSP-ready MSOCM board wherein an interface between said bigger opening and a smaller opening at a bottom portion of said board bonding-wire window providing a platform for disposing said board bonding-pads thereon.

8. The MSOCM assembly of claim 1 further comprising:

a liquid encapsulation filler filling each of said board bonding-wire windows; and

each of said board bonding-wire windows further comprising a liquid encapsulation dam disposed on said top surface of said CSP-ready MSOCM board surrounding said board bonding-wire window for keeping said liquid encapsulation filler from flowing outside of said liquid encapsulation dam.

9. The MSOCM assembly of claim 1 wherein:

said CSP-ready MSOCM board is a metal core CSP-ready MSOCM board.

10. The MSOCM assembly of claim 1 wherein:

said CSP-ready MSOCM board is a laminated multiple-layered board; and

said CSP-ready MSOCM board further having a plurality of built-in passive circuit elements supported on said CSP-ready MSOCM board.

11. A chip-size package (CSP)-ready multiple substrate-on-chip module (MSOCM) board comprising:

a top surface and a bottom surface;

a plurality of bonding-wire windows with a plurality of board-boarding pads disposed on said top surface surrounding each of said bonding wire windows;

a CSP-ready land-grid array comprising a plurality of land-grind contact pads disposed on said top surface of said CSP-ready MSOCM board wherein said plurality of land-grid contact pads are arranged to have a standard CSP-footprint for packaging IC chips provided to mount onto said CSP-ready MSOCM whereby each of said IC chips when mounted onto said CSP-ready MSOCM board is provided to be separated into an individual CSP package having said standard CSP footprint.

12. The CSP-ready MSOCM board of claim 11 further comprising:

a plurality CSP-ready separation lines for separating said CSP-ready MSOCM board when mounted with said IC chips into a plurality of standard individual CSP packages each contain one of said IC chips.

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13. The CSP-ready MSOCM board of claim 11 further comprising:
- a plurality of via connectors penetrating said CSP-ready MSOCM board and in electrical connection with each of said IC chips via metal traces disposed on said bottom surface of said MSOCM board; and
- each of said via connectors further being in electric connection with one of said land grid contact pads disposed on said top surface of said CSP-ready MSOCM board.
14. The CSP-ready MSOCM board of claim 11 wherein:
- said land grid array comprising said plurality of land grid contact pads constituting CSP-ready solder pads are insulated by a plurality of solder masks disposed between said land grid contact pads.
15. The CSP-ready MSOCM board of claim 11 further comprising:
- a plurality of solder balls, each mounted on one of said land grid contact pads on said top surface of said CSP-ready MSOCM board.
16. The CSP-ready MSOCM board of claim 11 further comprising:
- a plurality of testing pins including a set of burn-in test pins and a set of board level test pins disposed on an edge of said CSP-ready MSOCM board provided for conducting a plurality of burn-in and board level tests.

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17. The CSP-ready MSOCM board of claim 11 wherein:

each of said board bonding-wire windows further comprising a bigger opening near said top surface of said CSP-ready MSOCM board wherein an interface between said bigger opening and a smaller opening at a bottom portion of said board bonding-wire window providing a platform for disposing said board bonding-pads thereon.

18. The CSP-ready MSOCM board of claim 11 further comprising:

a liquid encapsulation filler filling each of said board bonding-wire windows; and

each of said board bonding-wire windows further comprising a liquid encapsulation dam disposed on said top surface of said CSP-ready MSOCM board surrounding said board bonding-wire window for keeping said liquid encapsulation filler from flowing outside of said liquid encapsulation dam.

19. The CSP-ready MSOCM board of claim 11 wherein:

said CSP-ready MSOCM board is a metal core CSP-ready MSOCM board.

20. The CSP-ready MSOCM board of claim 11 wherein:

said CSP-ready MSOCM board is a laminated multiple-layered board; and

a plurality of built-in passive circuit elements supported on said CSP-ready MSOCM board.

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21. A chip-size package (CSP)-ready multiple substrate-on-chip module (MSOCM) board, having a top surface and bottom surface, comprising:

a plurality of bonding-wire windows with a plurality of board-boarding pads disposed on said top surface surrounding each of said bonding wire windows;

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a plurality of CSP-ready contact pads disposed on said top surface of said CSP-ready MSOCM board wherein said plurality of contact pads are arranged to have a standard CSP-footprint for packaging IC chips provided to mount onto said CSP-ready MSOCM board whereby each of said IC chips when mounted onto said CSP-ready MSOCM board is provided to be separated into an individual CSP package having said standard CSP footprint.